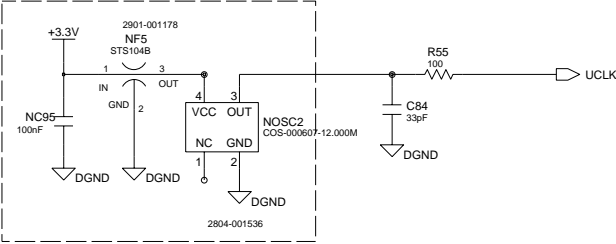
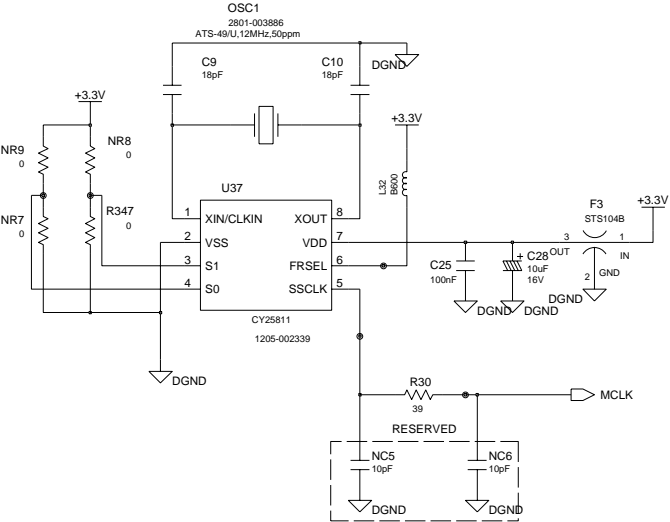


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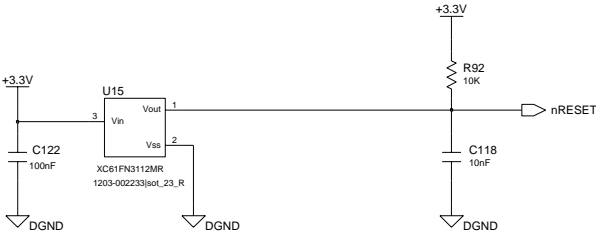


MHz	FRSEL	S1=0 S0=0	S1=0 S0=M	S1=0 S0=1	S1=M S0=0
12-14	1	1.2%	0.9%	0.5%	0.4%

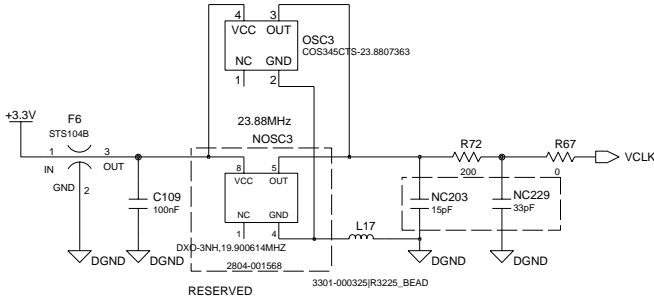
* M= Floating



[MAIN SYSCLK]

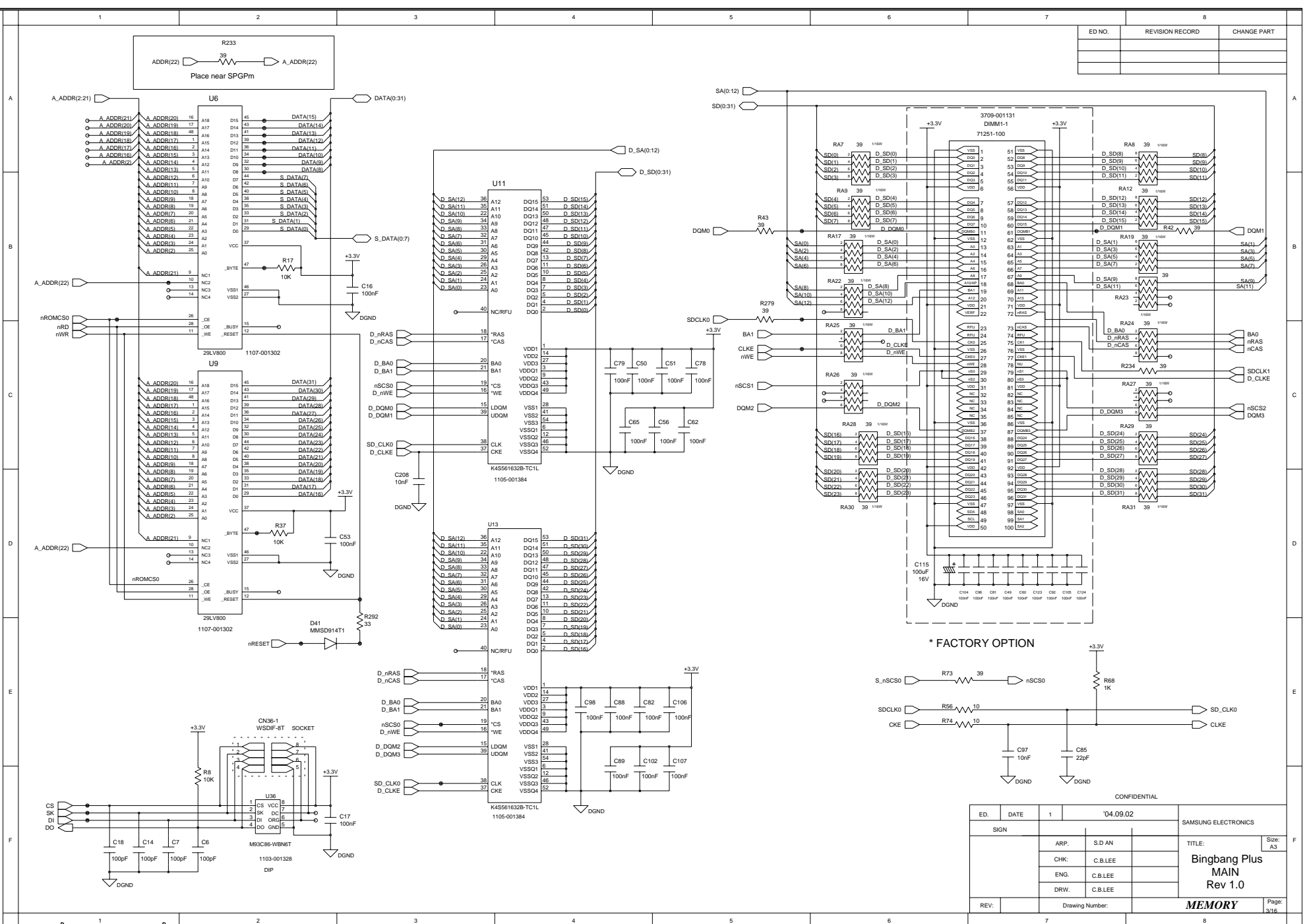


[RESET]

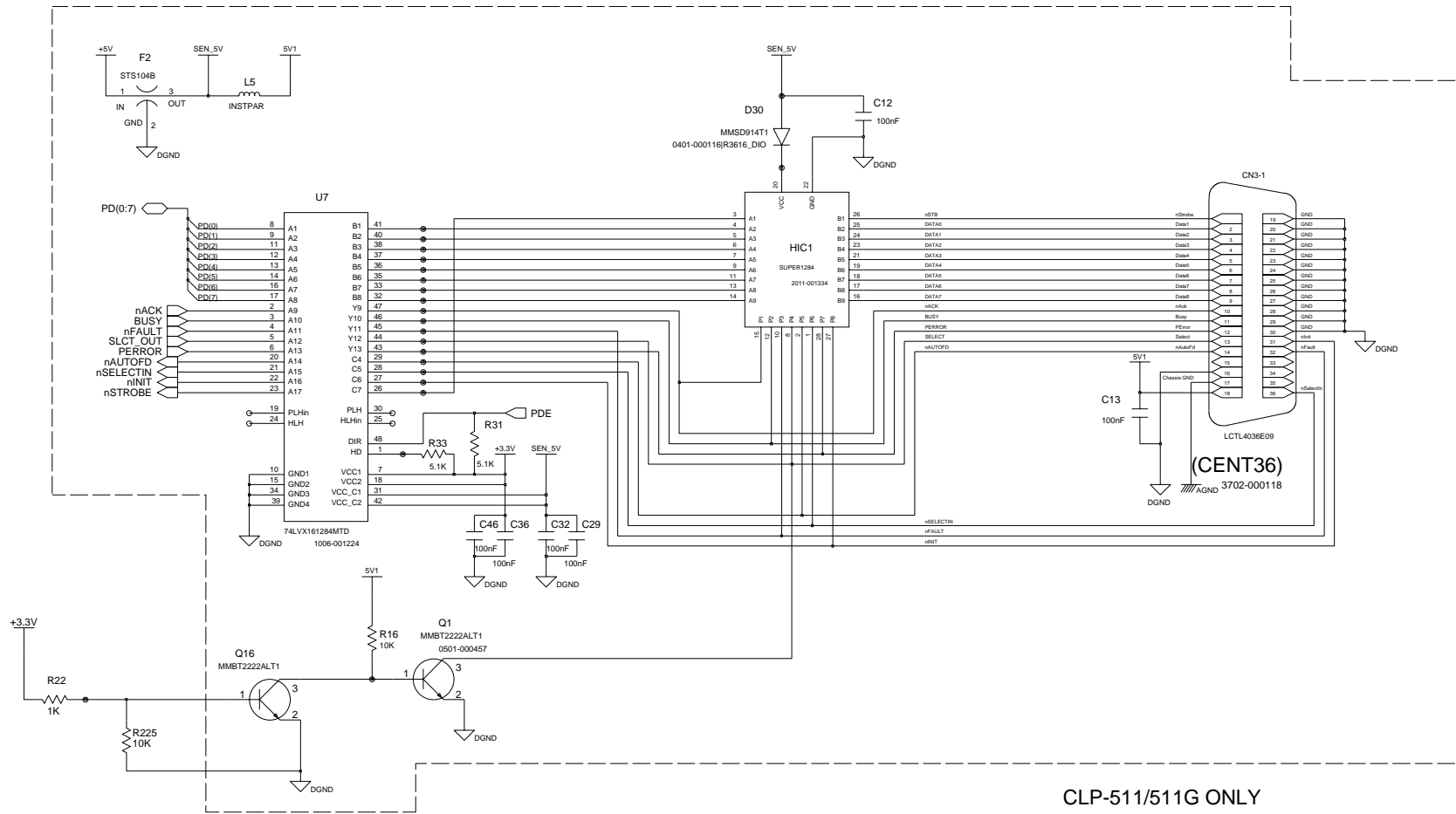


[VCLK SYSCLK]

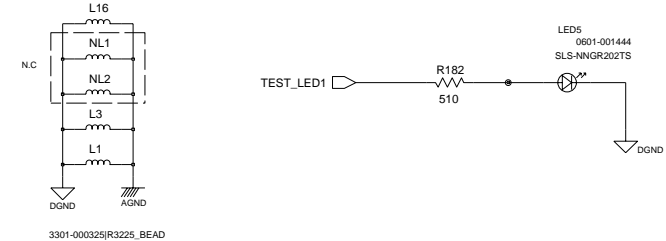
CONFIDENTIAL				SAMSUNG ELECTRONICS	
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SIGN				ARP.	S.D AN
				CHK:	C.B.LEE
				ENG:	C.B.LEE
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REV:				Drawing Number:	
				Bingbang Plus MAIN Rev 1.0	
				RESET & CLK	
				Page: 2/16	



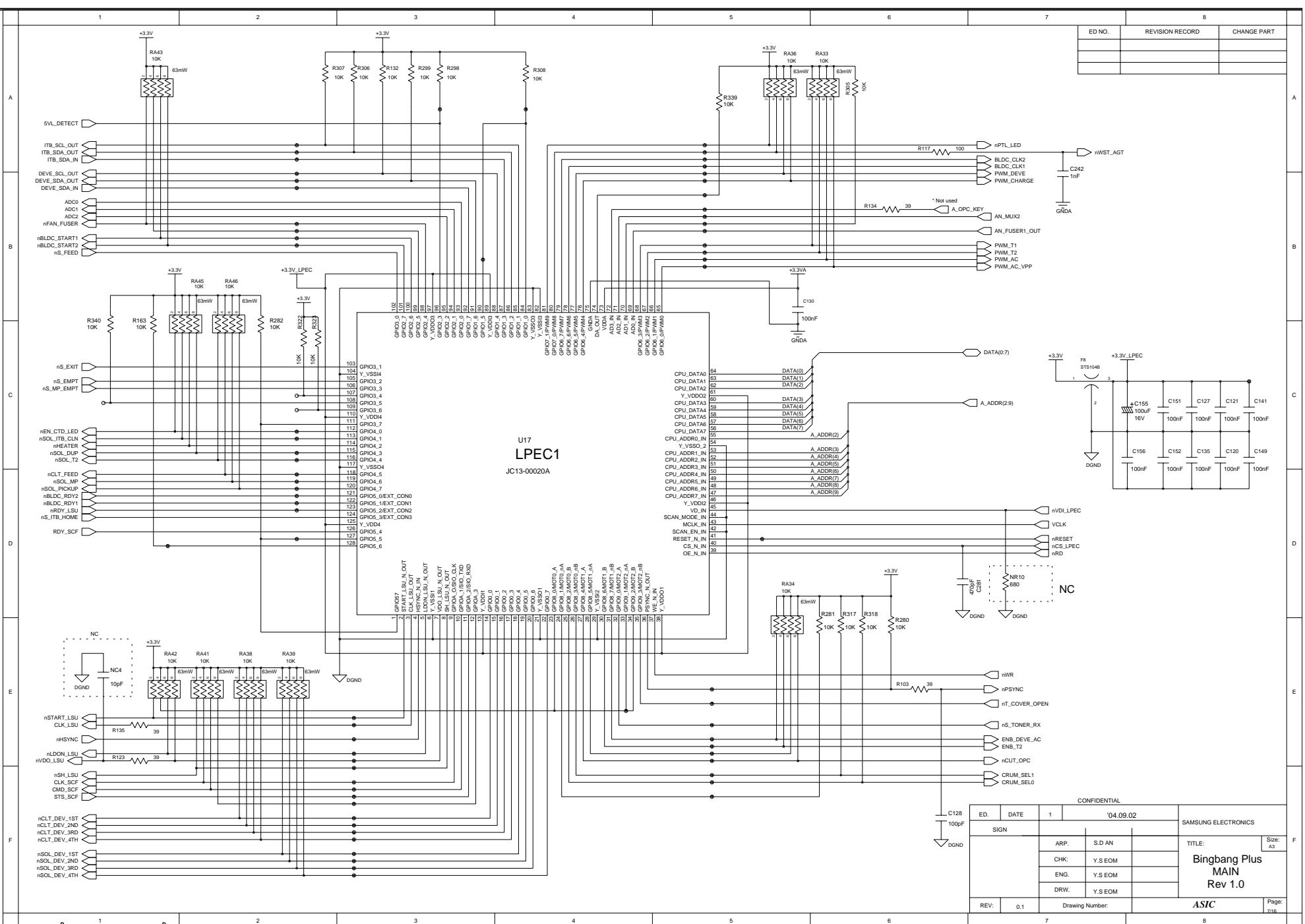
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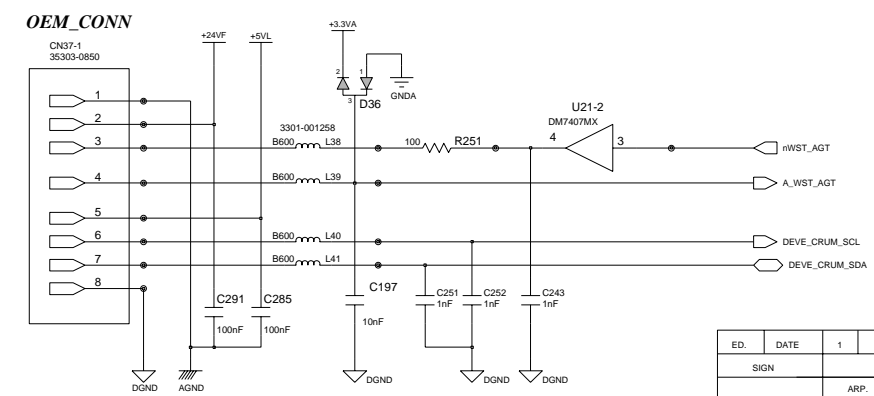
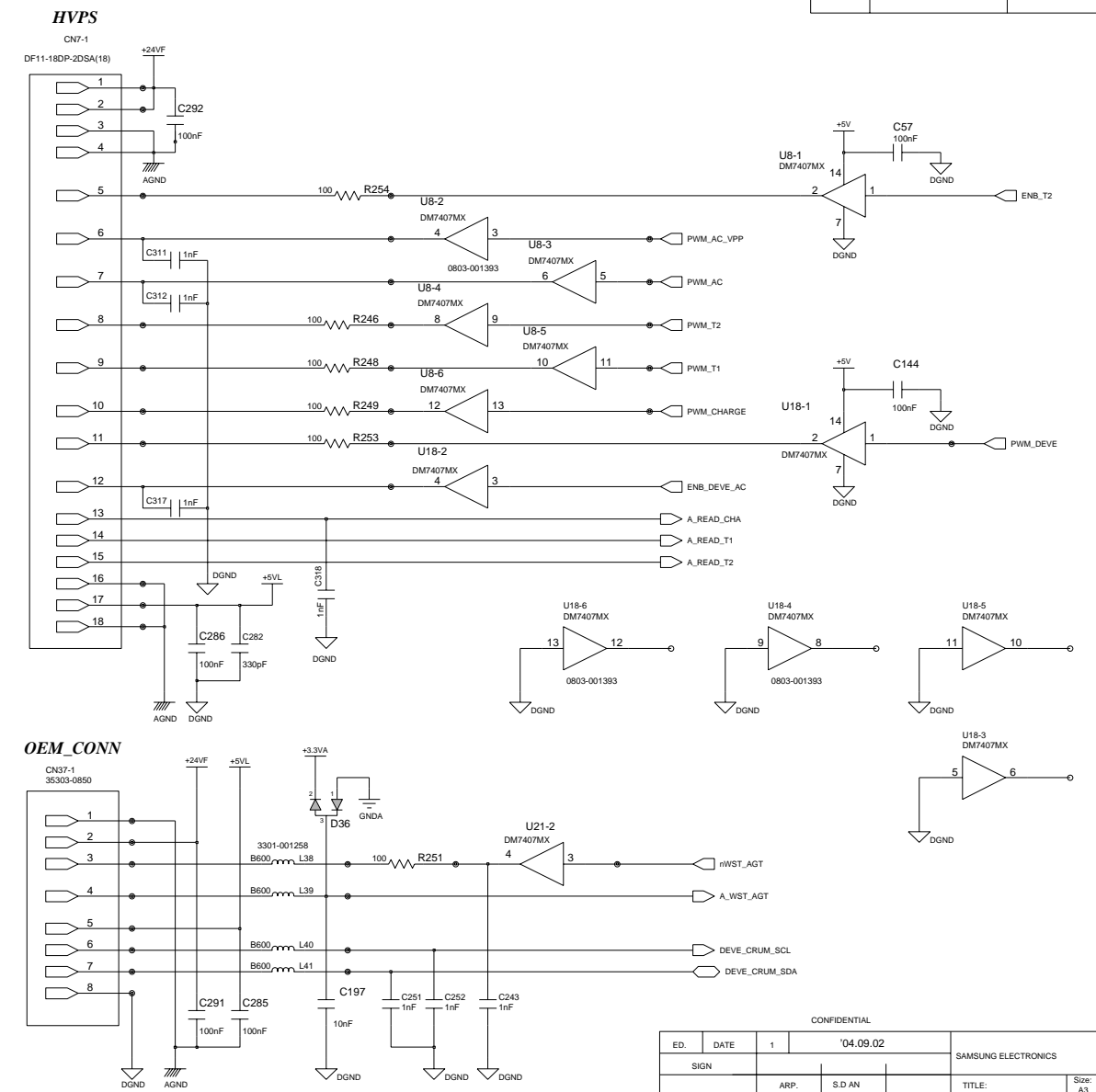
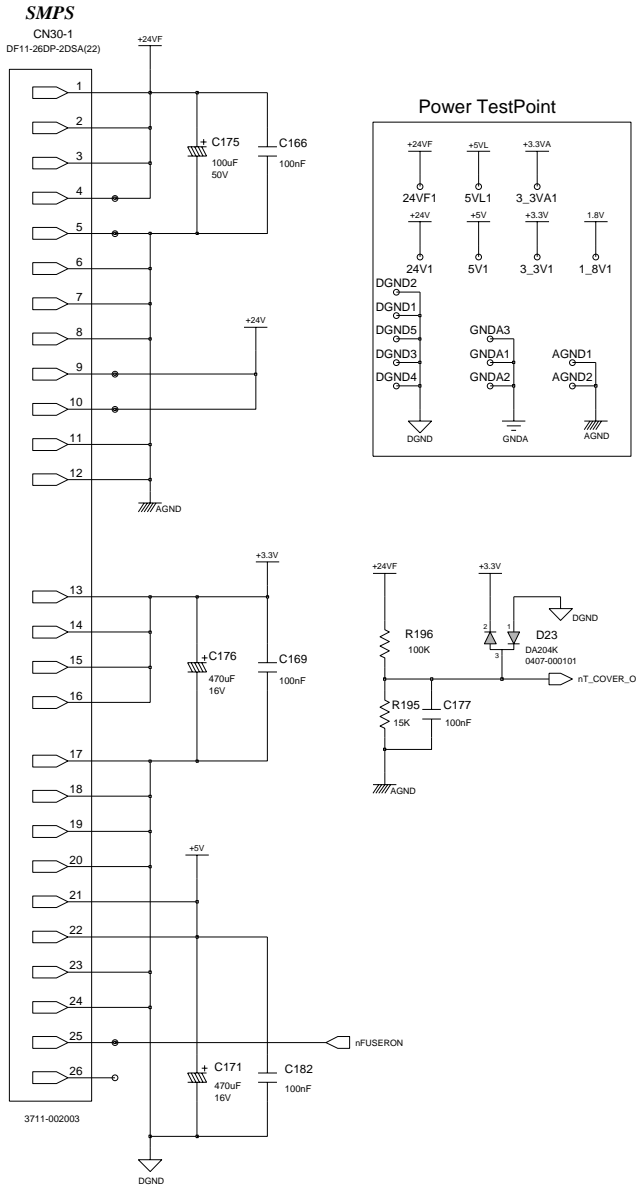


CLP-511/511G ONLY



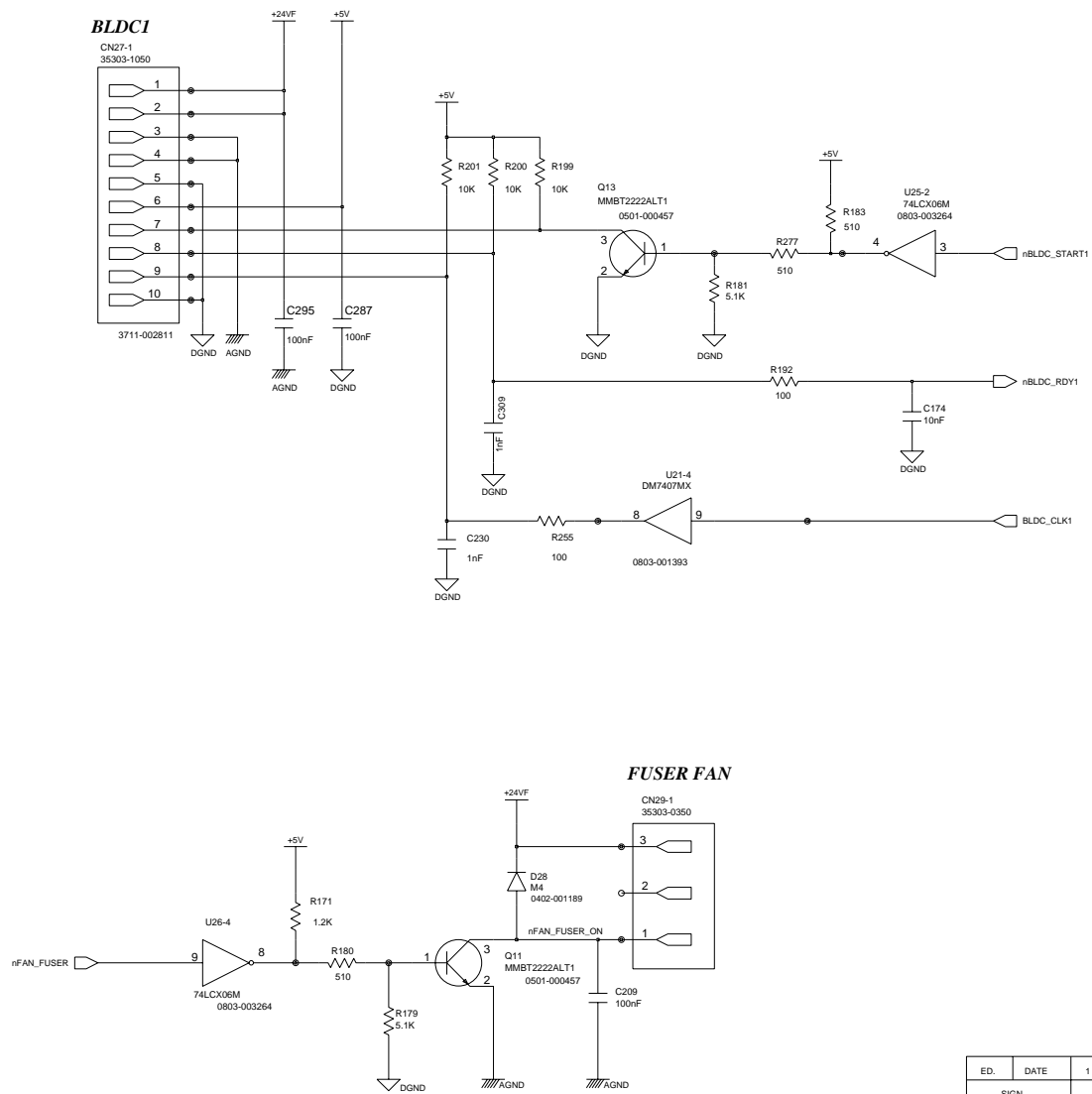
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		ENG.	C.B.LEE						
		DRW.	C.B.LEE						
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				Page: 6/16					





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		DRW:	Y.S EOM	SMPS & HVPS					
REV:		Drawing Number:		Page: 3/16					

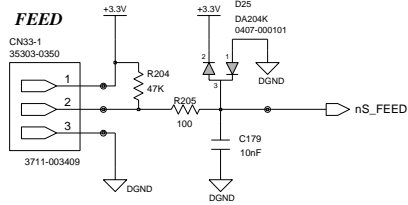
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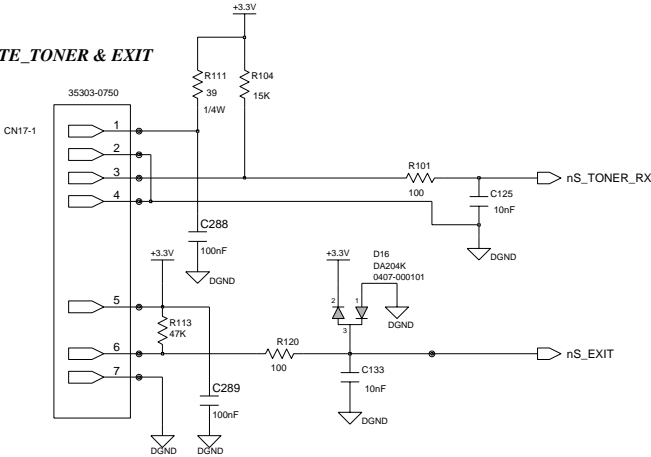
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REV:		Drawing Number:		MOTOR	
				Page: 10/16	

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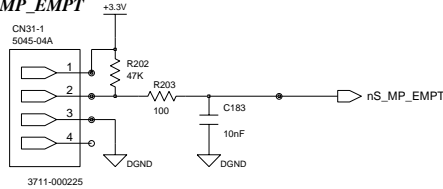
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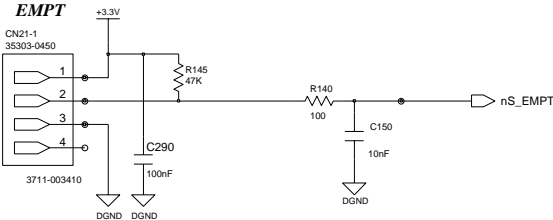
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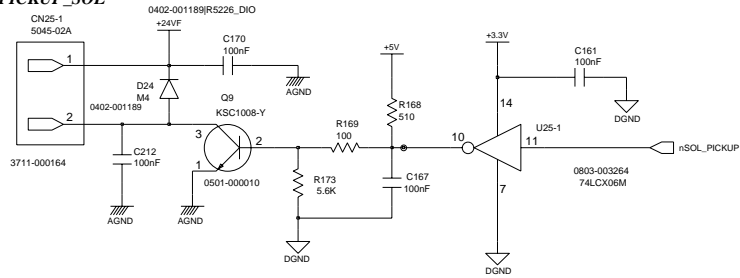


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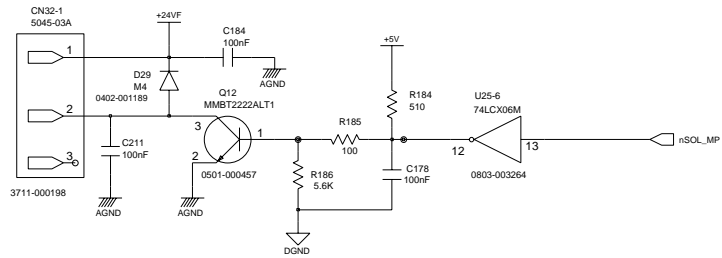


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				Page: 11/16	

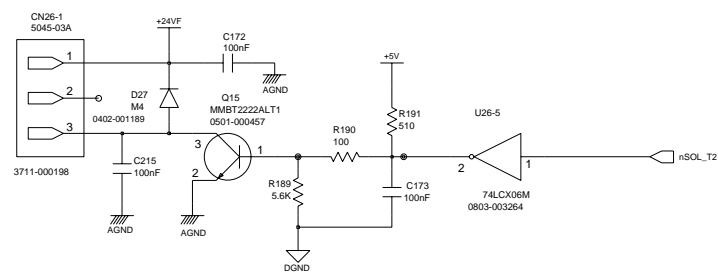
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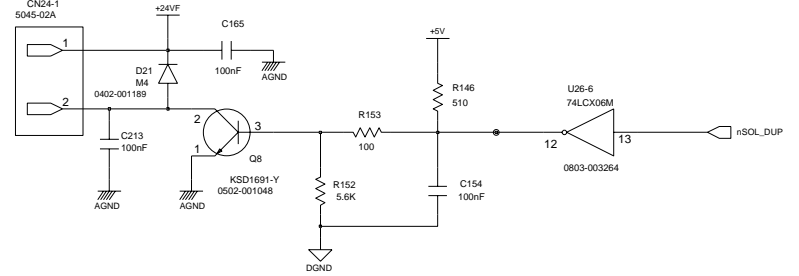
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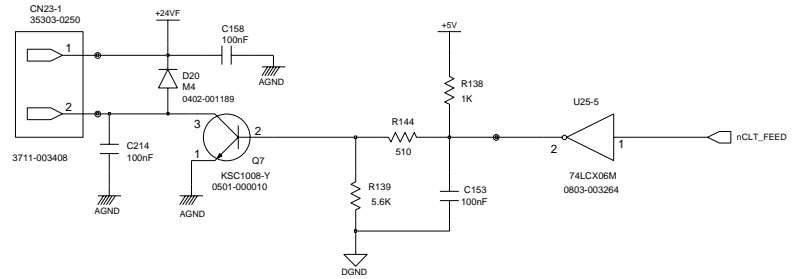
T2_HOME



DUPLEX



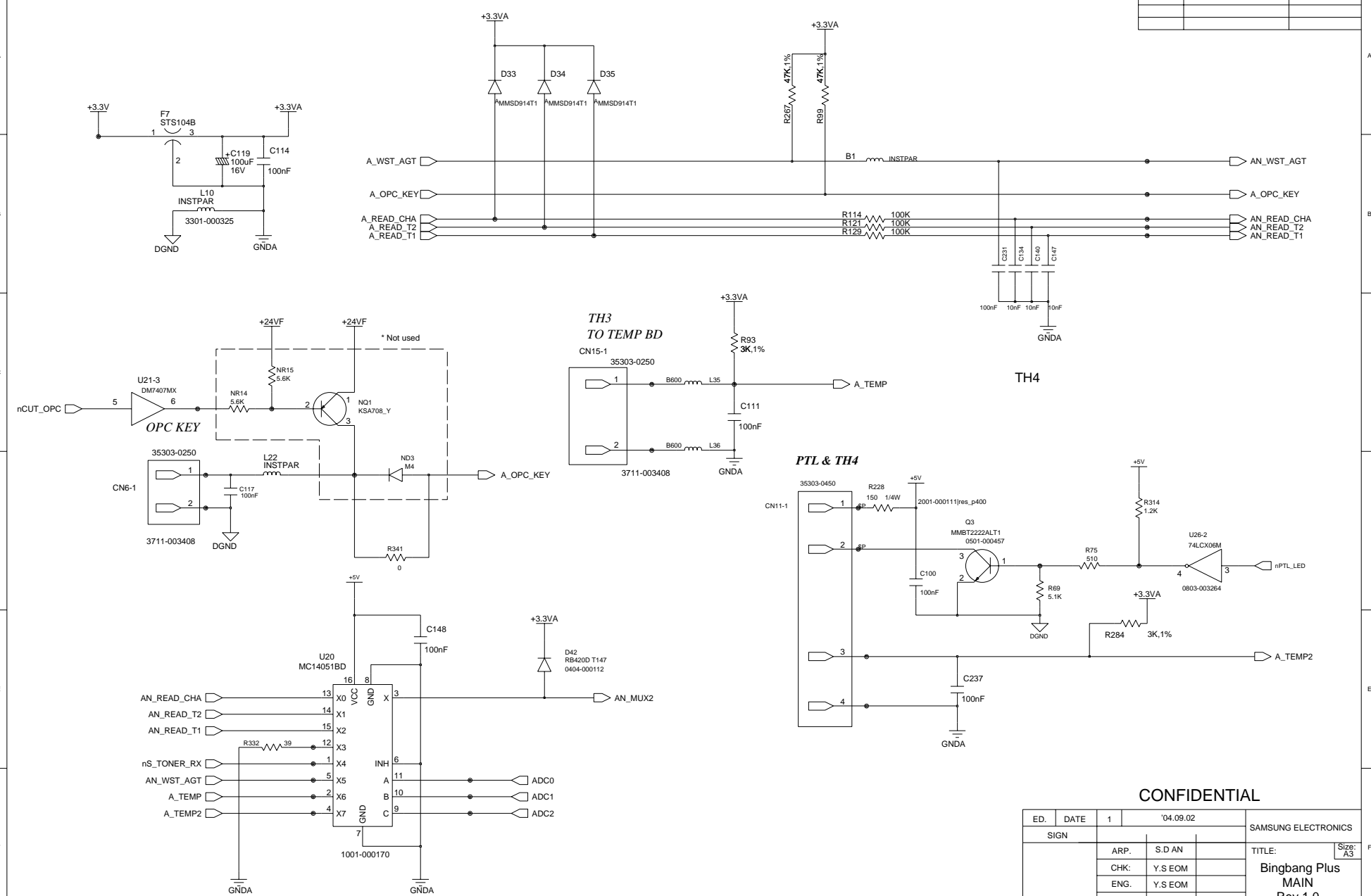
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REV:	Drawing Number:			SOLENOID
				Page: 12/16

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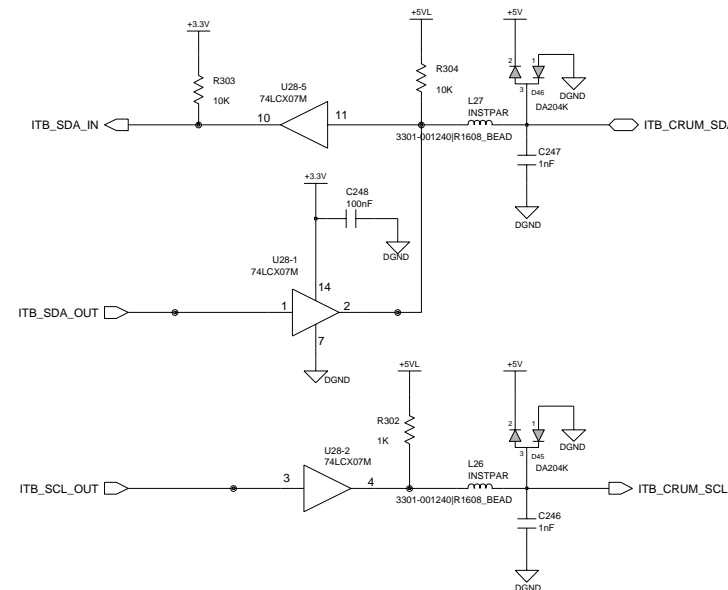
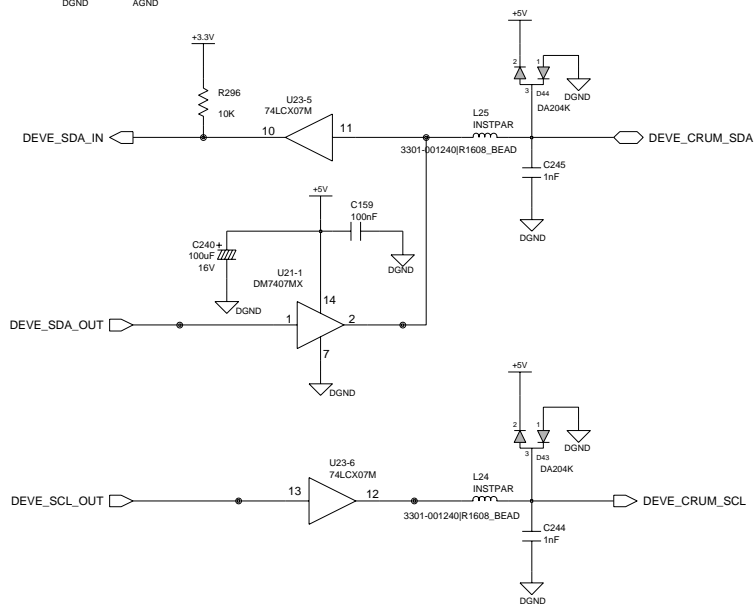
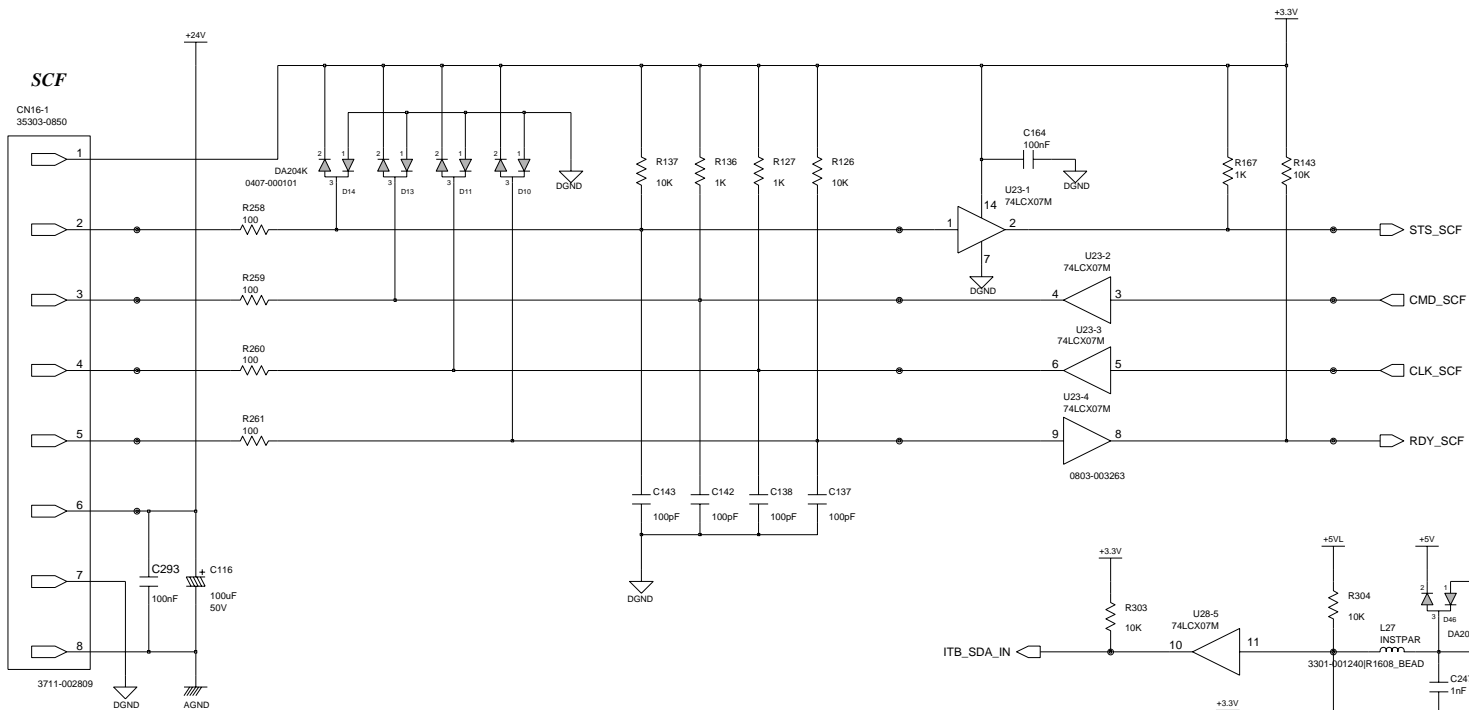
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		Drawing Number:		ANALOG MUX		
				Page: 14/16		

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SIGN									
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		CHK.	Y.S EOM						
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REV:		Drawing Number:			SCF, CRUM Interface				Page: 15/16

